

IN THE UNITED STATES PATENT AND TRADEMARK OFFICESelby
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8-13-02

Application Serial No. 09/603,147
Filing Date June 23, 2000
Inventor John T. Moore et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Kielin, E.
Attorney's Docket No. MI22-1443
Title: Capacitor Constructions, DRAM Constructions and Semiconductive Material Assemblies

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INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

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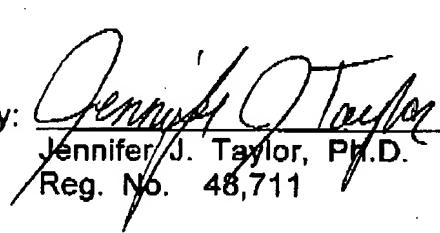
The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Information Disclosure Statement is being filed before the mailing date of a first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 6-27-2002

By: 
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

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**Assistant Commissioner for Patents
Washington, D.C. 20231**

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CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9318 on the date shown below:

1. Certificate of Facsimile Transmission
2. Information Disclosure Statement w/PTO-1449 and a copy of the cited reference

Dated: June 27, 2002

By: Jaime M. White
Jaime M. White
Telephone No. (509) 624-4276
Facsimile No. (509) 838-3424

NUMBER OF PAGES IN FACSIMILE: 25